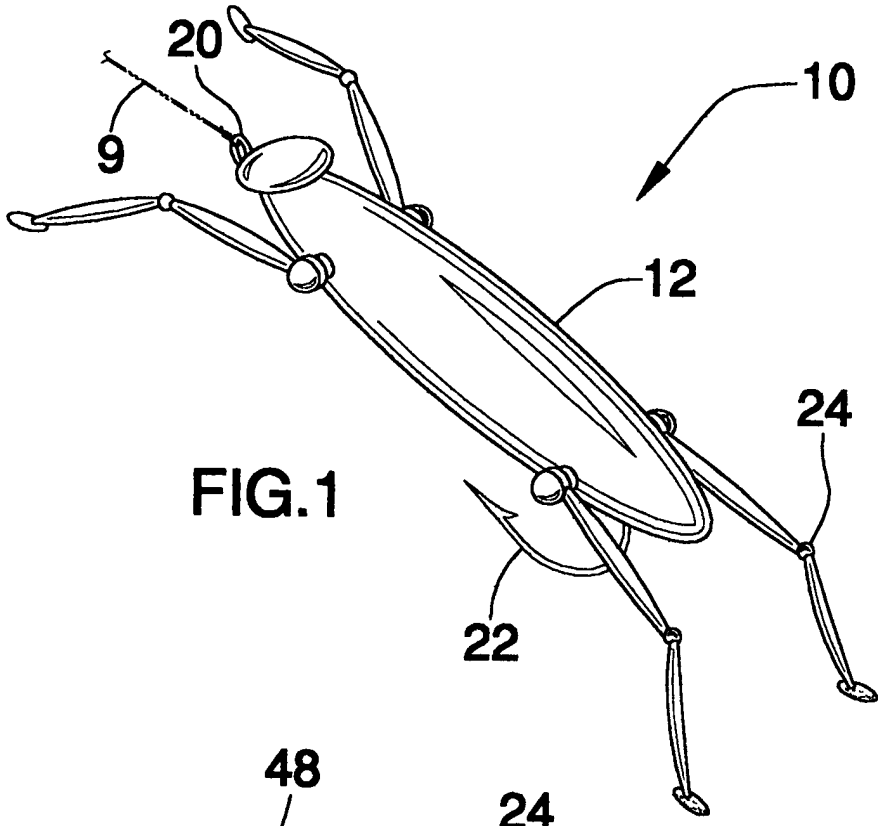


FIG.1

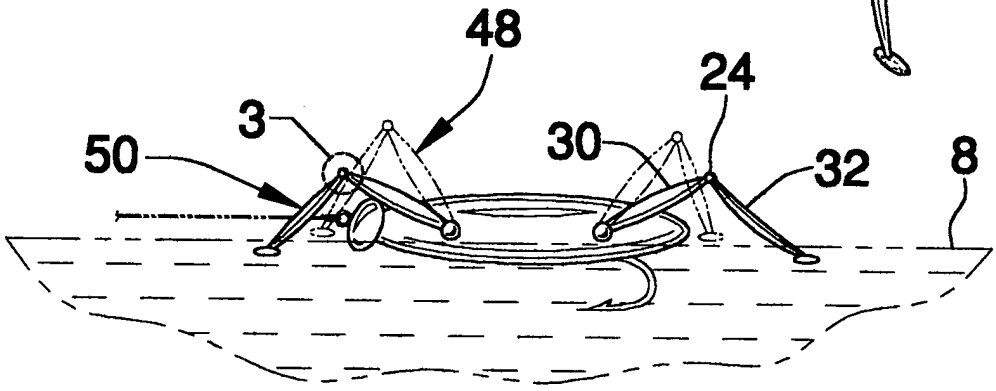


FIG.2

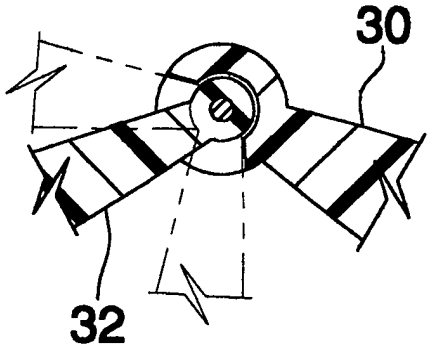


FIG.3

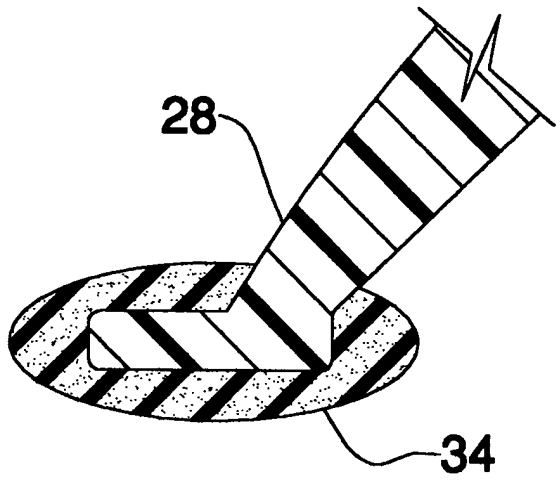


FIG.4

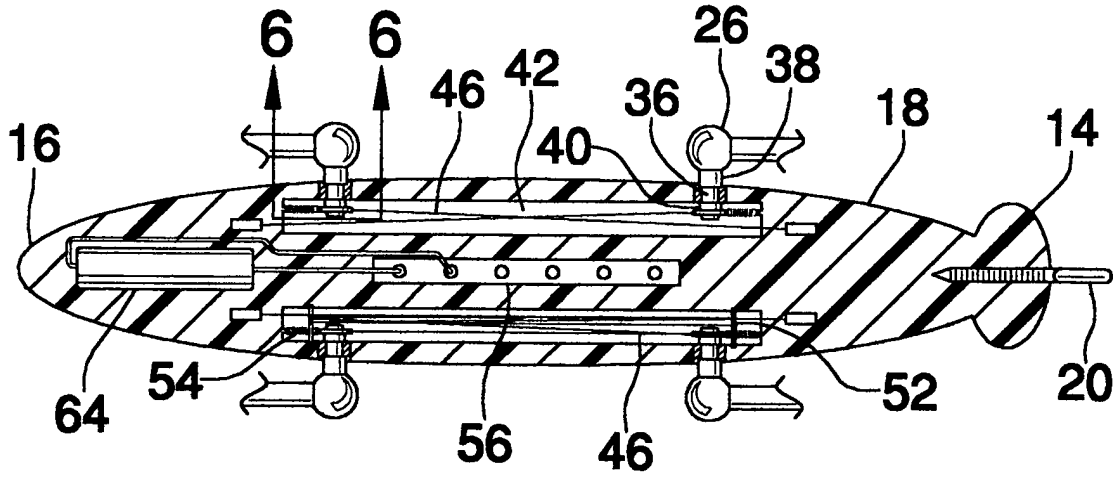
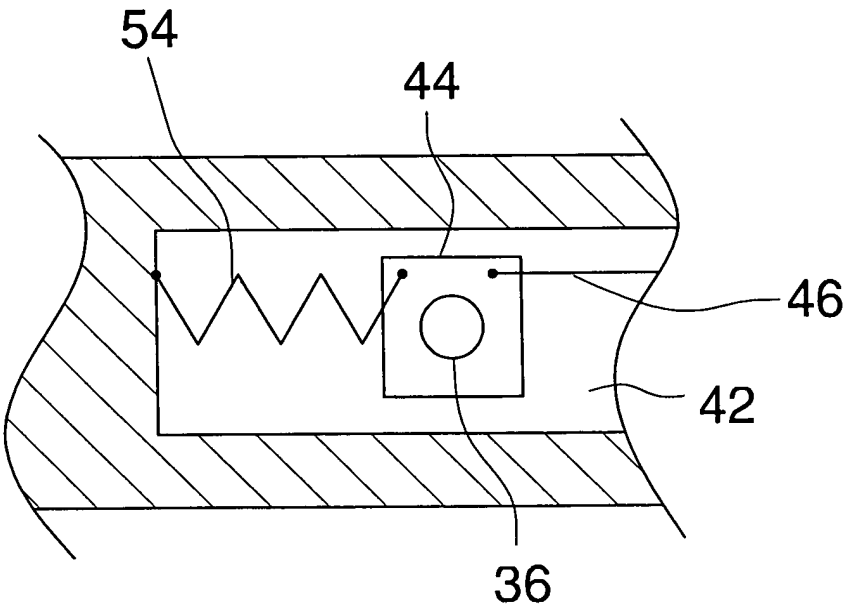


FIG.5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44
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42
36

FIG.6

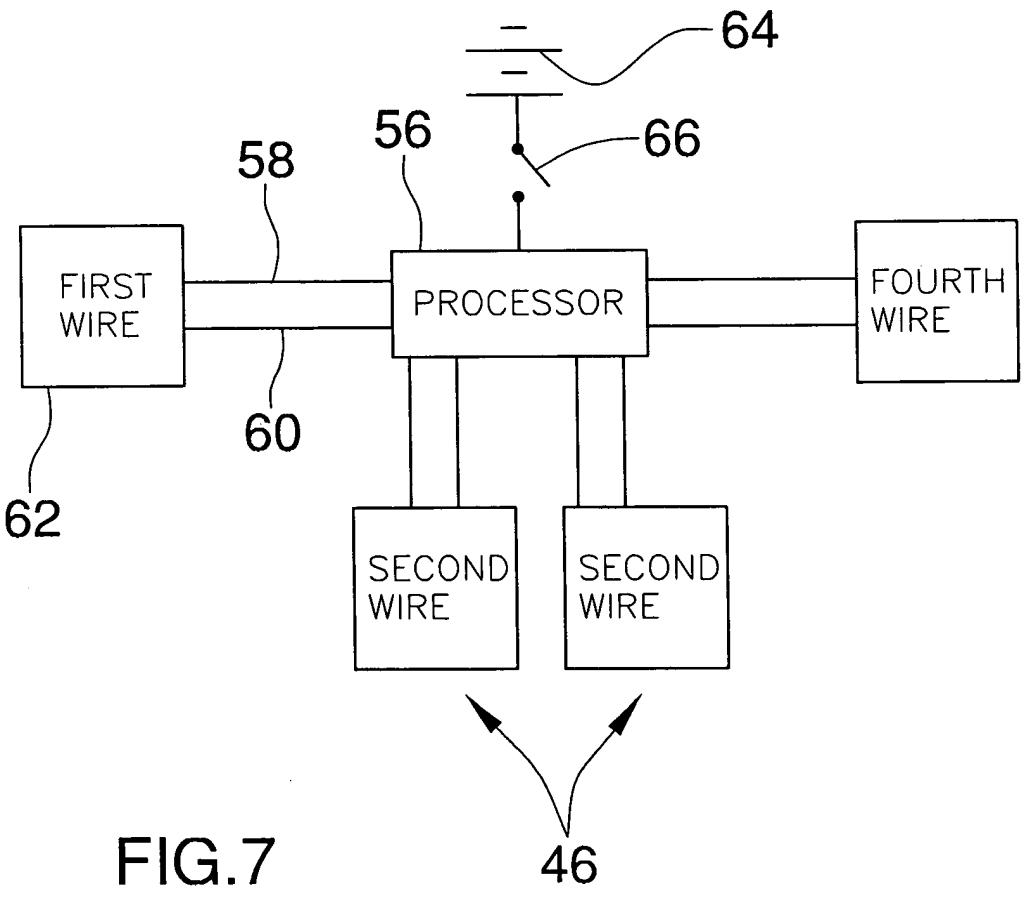
64
56
66
58
FIRST WIRE
PROCESSOR
FOURTH WIRE
60
62
SECOND WIRE
SECOND WIRE
46

FIG.7